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SK TC116x-Series Starter Kit

MCU Derivates:

- SAF-TC1161-128F66HL
- SAF-TC1162-128F66HL
- SAF-TC1163-128F80HL
- SAF-TC1164-128F80HL
- SAF-TC1165-192F80HL
- SAF-TC1166-192F80HL

CPU Clock: 66/80 MHz

On-Chip Memory:

- 1.0/1.5 MByte embedded program flash with ECC,
- 16/32 KByte data flash for scalable EEPROM emulation,
- 32/40/56 KByte on-chip SRAM,
- 8 KByte instruction cache.
- 8/16 Kbyte code scratchpad memory

Interfaces

- DB9 for RS232-0,
- BERG10 for RS232-1,
- two BERG10 for CAN-0/1 with Transceiver,
- BERG16 for OCDS1,
- SAMTEC QSH-030-01-F-D-A for OCDS2,
- DB25 On Board Wiggler for OCDS1,
- three four 80-pin connectors (male) with all I/O signals,
- three four 80-pin connectors (female) with all I/O signals.

Includings:

- TC116x User manual (pdf),
- TriCore Architecture manual (pdf),
- TriBoard manual (pdf),
- Tools: Compiler, Debugger, Operating System from Infineon Tool Partners(Evaluation versions)
- Cable,
- 1 Extension Board,
- Power plug